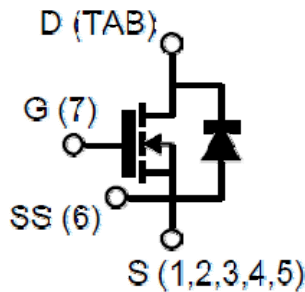
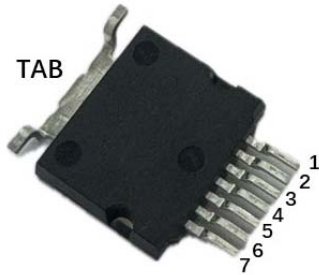


## Silicon Carbide Power MOSFET (N-Channel Enhancement)

$V_{DS}$	650V
$I_D$ (25°C)	34A
$R_{DS(on)}$	100mΩ



### Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant
- AEC-Q101 qualified

### Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

### Mechanical Data

- **Package:** T2PAK
- **Terminals:** Tin plated leads
- **Polarity:** As marked

### ■Maximum Ratings ( $T_C=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D2065100T2GH		
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	650	$V_{GS}=0\text{ V}$ , $I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-10/+25	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	Note1
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-5/+20	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	$I_D$	A	34	$V_{GS}=20\text{V}$ , $T_c=25^\circ\text{C}$	Fig.14
Continuous drain current @ $T_c=110^\circ\text{C}$			23	$V_{GS}=20\text{V}$ , $T_c=110^\circ\text{C}$	
Pulse Drain Current	$I_{D,pulse}$	A	62	Limited by $t_{pw}$	Fig.15
Avalanche energy, Single Pulse	$E_{AS}$	J	0.8	$V_{DD}=100\text{V}$ , $I_D=7\text{A}$	
Power Dissipation	$P_{TOT}$	W	170	$T_c=25^\circ\text{C}$ , $T_j = 175^\circ\text{C}$	Fig.13
Operating junction and Storage temperature range	$T_j, T_{stg}$	°C	-55 to +175		
Soldering temperature	$T_L$	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	$T_M$	Nm	1.0	M3 screw Maximum of mounting process: 3	



### ■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V		2.6		$V_{DS}=V_{GS}$ , $I_D=10mA$	Fig.4, 11
Drain source breakdown voltage	$V_{(BR)DSS}$	V	650			$V_{GS}=0$ , $I_D=100\mu A$	
Zero gate voltage drain current	$I_{DSS}$	$\mu A$		<1	50	$V_{DS}=650V$ , $V_{GS}=0V$	
				5	500	$V_{DS}=650V$ , $V_{GS}=0V$ , $T_j=175^\circ C$	
Gate source leakage current	$I_{GSS}$	nA			250	$V_{GS}=20V$ , $V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS(on)}$	m $\Omega$		100	130	$V_{GS}=20V$ , $I_D=12A$	Fig.3, 5, 6
				130		$V_{GS}=20V$ , $I_D=12A$ , $T_j=175^\circ C$	
Transconductance	$g_f$	S		8.5		$V_{DS}=15V$ , $I_D=25A$	

### ■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	$C_{iss}$	pF		910		$V_{DS}=400V$ , $V_{GS}=0V$ , $T_j=25^\circ C$ , $f=1MHz$ , $V_{AC}=25mV$	Fig.10
Output capacitance	$C_{oss}$			105			
Reverse capacitance	$C_{rss}$			13			
Coss stored energy	$E_{oss}$	$\mu J$		10			Fig.12
Gate source charge	$Q_{gs}$	nC		13.5		$V_{DS}=400V$ , $V_{GS}=-5/20V$ , $I_D=12A$	Fig.16
Gate drain charge	$Q_{gd}$			34			
Gate charge	$Q_g$			66			
Short-Circuit Withstand Time	$t_{sc}$	$\mu s$		<18		$V_{GS}=0/15V$ , $V_{DS}=400V$ $R_G=100\Omega$	
Internal Gate Resistance	$R_{G(int)}$	$\Omega$		3		$f=1MHz$ , $V_{AC}=25mV$	

### ■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		15		$V_{DD}=400V$ , $V_{GS}=0/+20V$ , $I_D=10A$ , $L=100\mu H$ $R_{g(ext)}=2.7\Omega$	Fig.17, 18
Rise time	$t_r$			17			
Turn off delay time	$t_{d(off)}$			15			
Fall time	$t_f$			15			
Turn on switching energy	$E_{on}$	$\mu J$		29.7			
Turn off switching energy	$E_{off}$			8.24			



■Body diode characteristics (Tc=25°C unless otherwise specified )

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V <sub>SD</sub>	V		3.3		V <sub>GS</sub> =0V, I <sub>SD</sub> =3A	Fig.8
Continuous diode forward current	I <sub>S</sub>	A		28.5		V <sub>GS</sub> =0V, Tc=25°C	
Reverse recovery time	t <sub>rr</sub>	nS		54		V <sub>DS</sub> =400V, V <sub>GS</sub> =0V, I <sub>SD</sub> =12A, di/dt=300A/uS	
Reverse recovery charge	Q <sub>rr</sub>	nC		72			
Peak reverse recovery current	I <sub>rrm</sub>	A		2.57			

Note 1: When using SiC Body Diode the maximum recommended V<sub>GS</sub> = -5V

■Thermal Characteristics (T<sub>a</sub>=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R <sub>θJ-C</sub>	°C/W	0.88

■Typical Characteristics

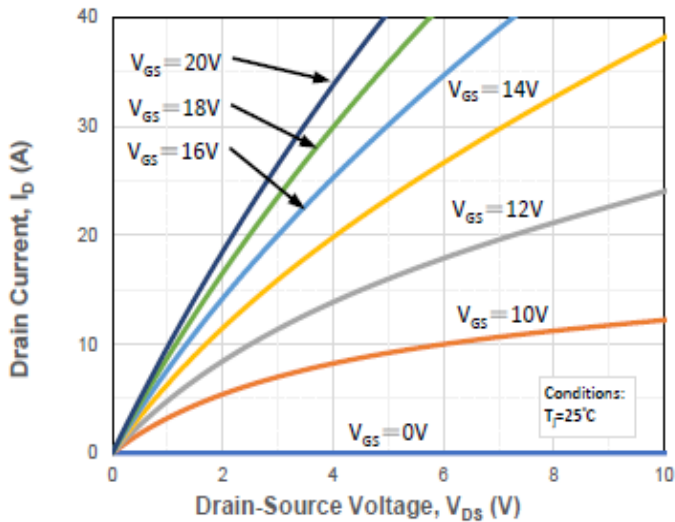


Figure 1. Output Characteristics Tj = 25°C

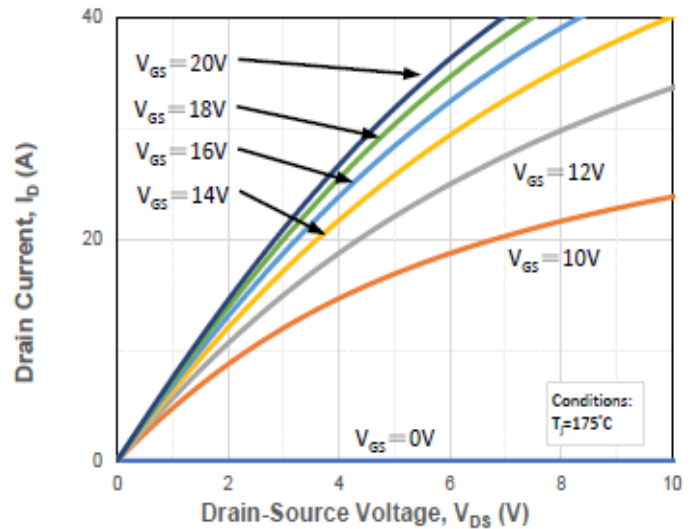
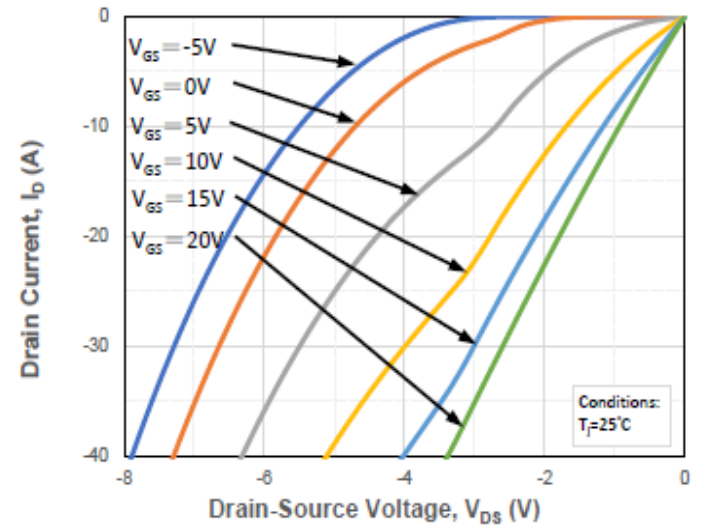
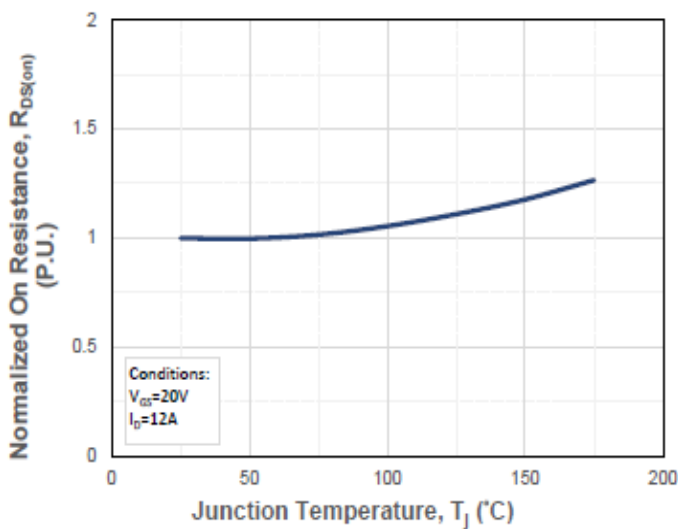
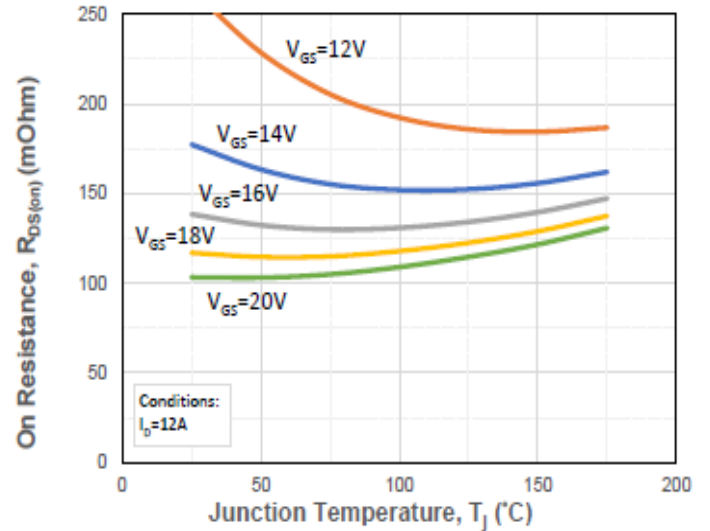
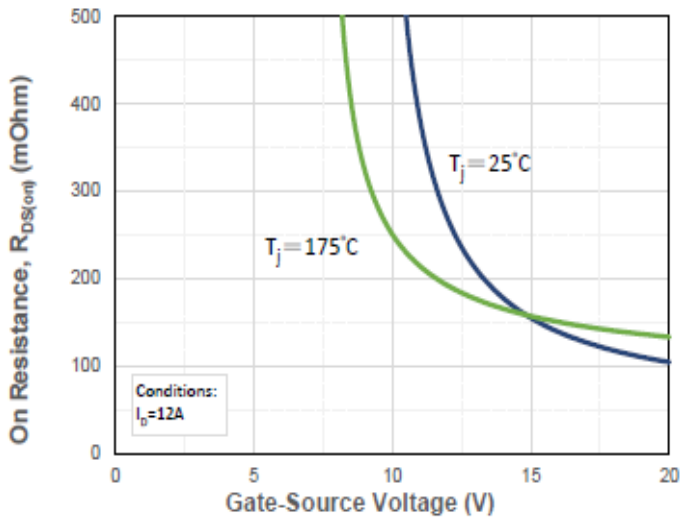
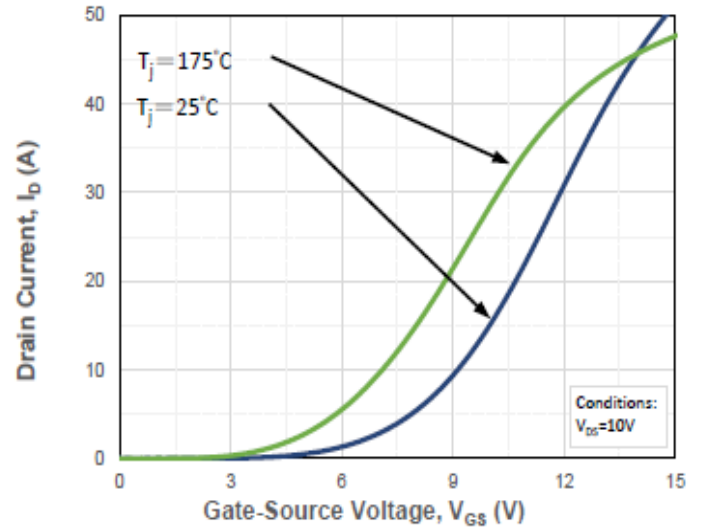
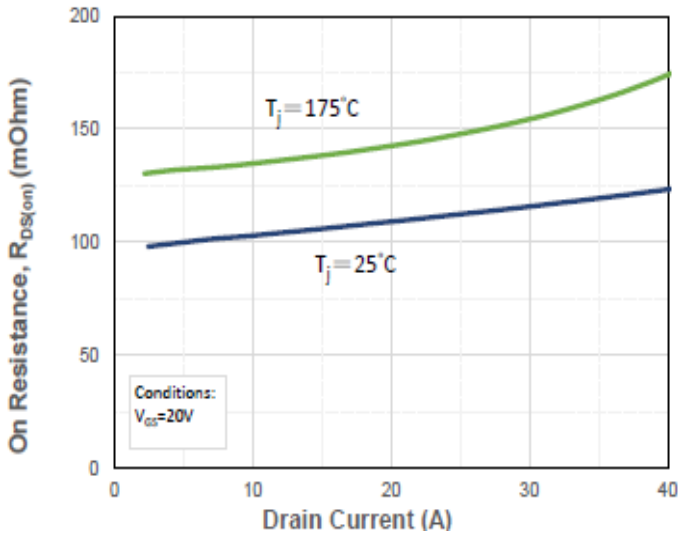


Figure2. Output Characteristics Tj = 175°C



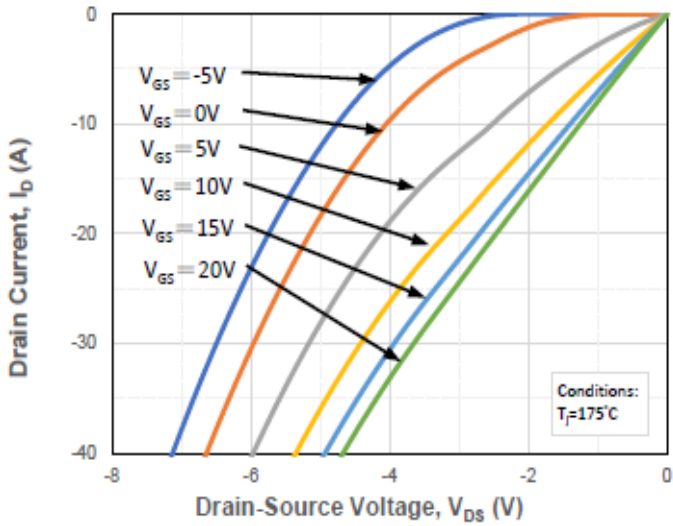


Figure 9. Reverse Output Characteristics at Tj = 175°C

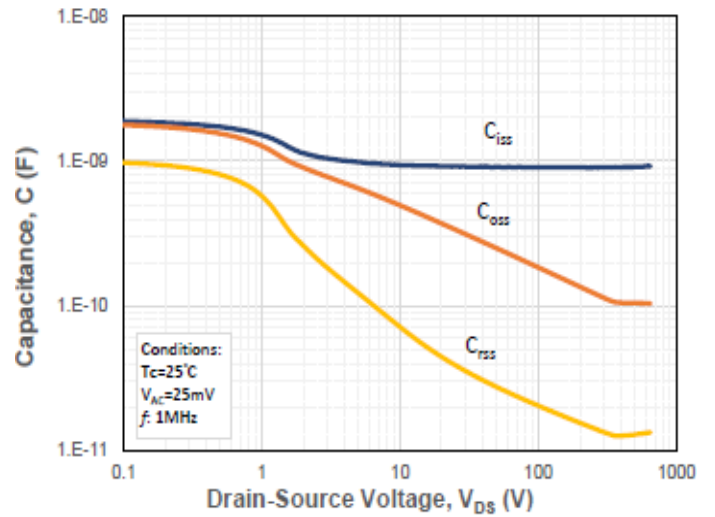


Figure 10. Capacitances vs. Drain to Source Voltage

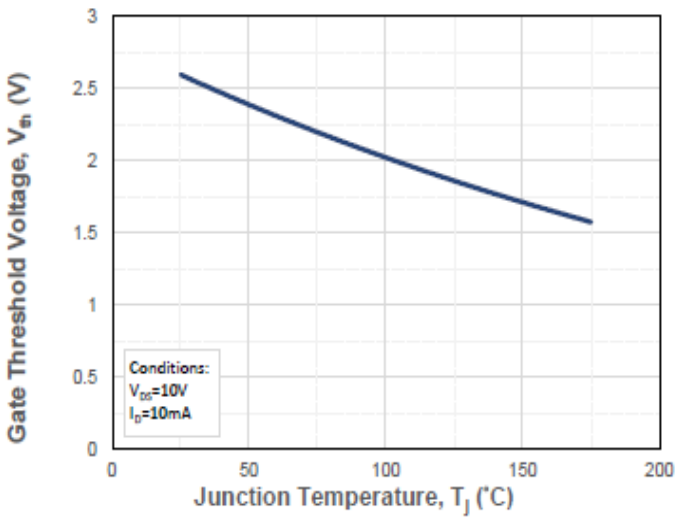


Figure 11. Threshold voltage vs. temperature

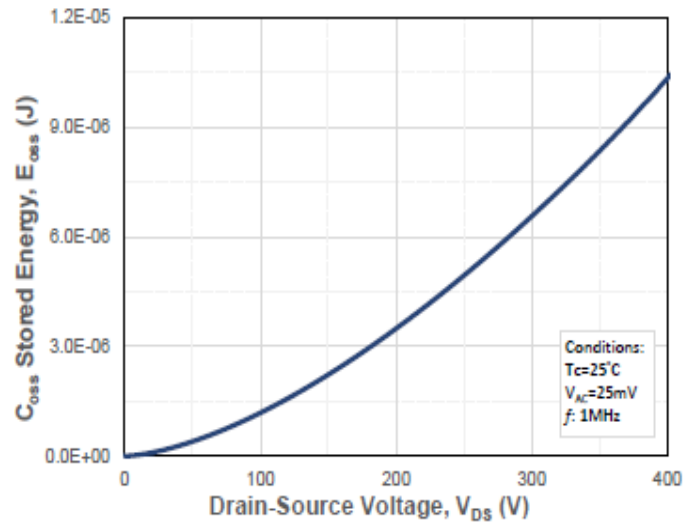


Figure 12. Output Capacitor Stored Energy

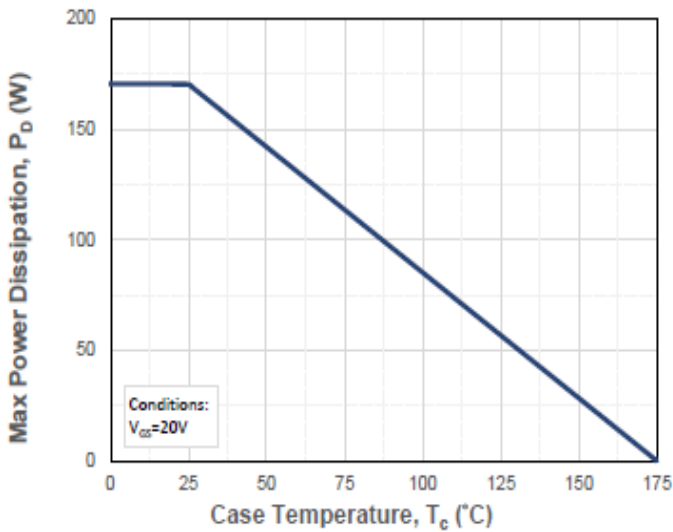


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

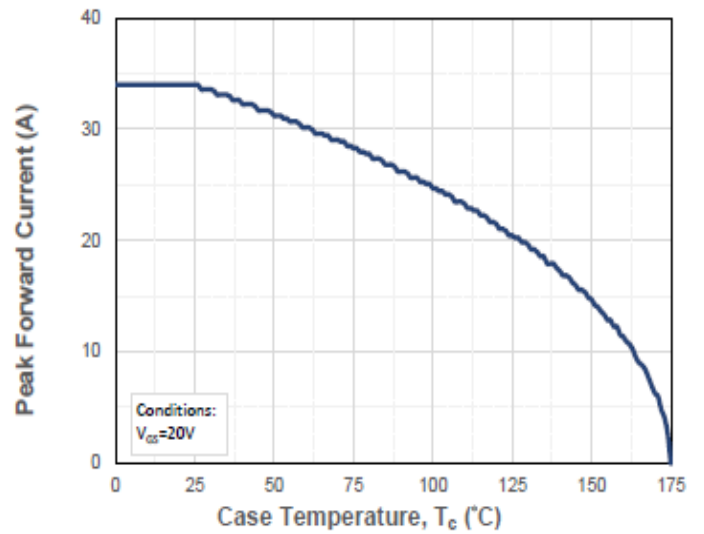


Figure 14. Drain Current Derating vs. Case Temperature

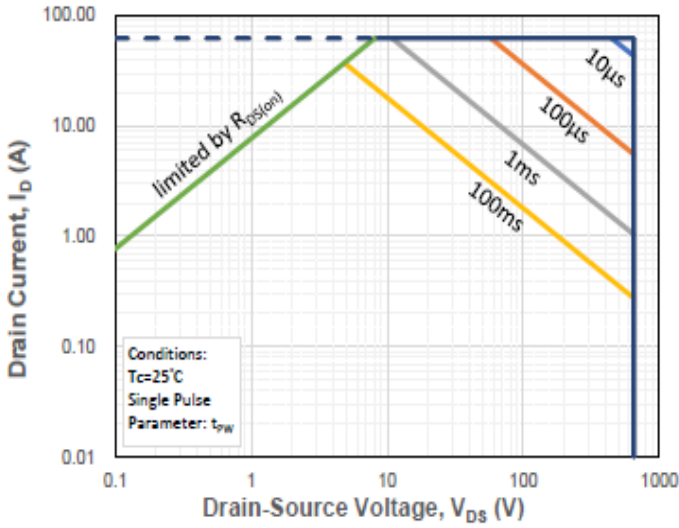


Figure 15. Safe Operating Area

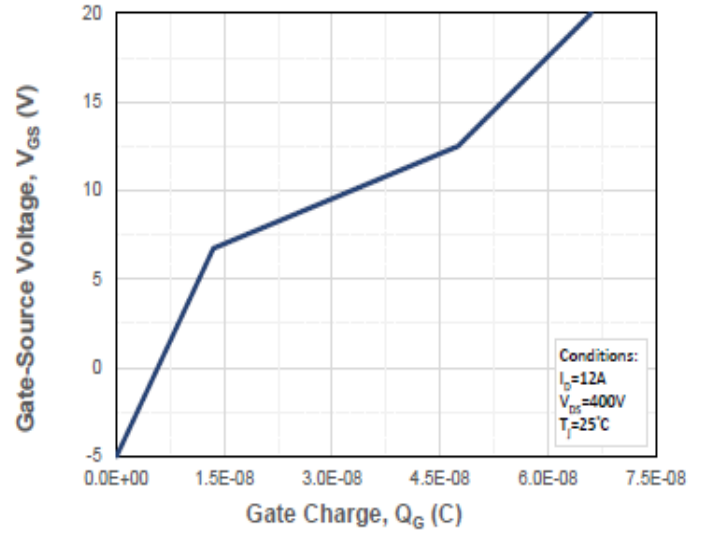


Figure 16. Gate Charge Characteristics

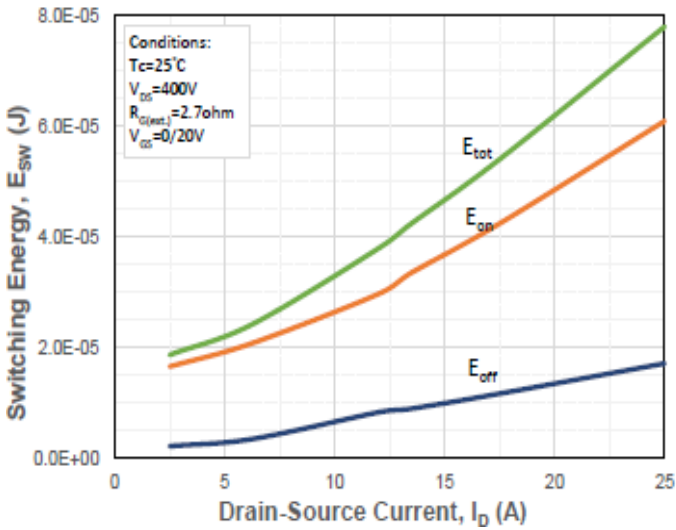


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

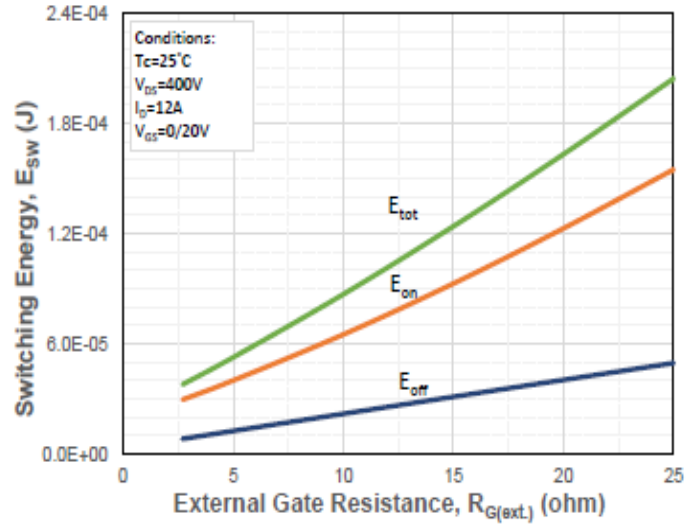


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor ( $R_{G(ext.)}$ )

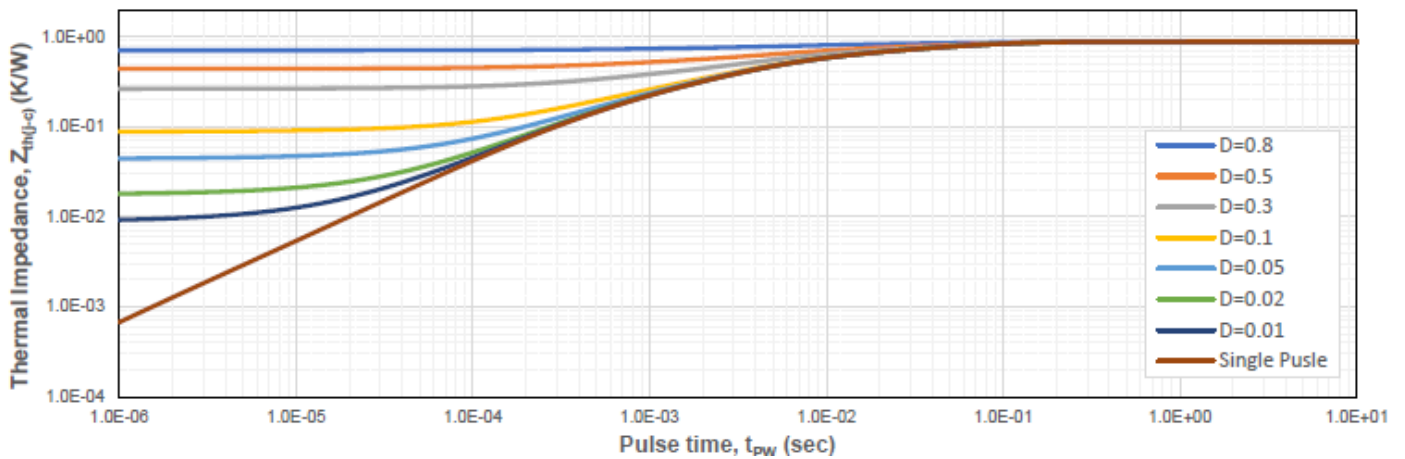


Figure 19. Transient Junction to Case Thermal Impedance

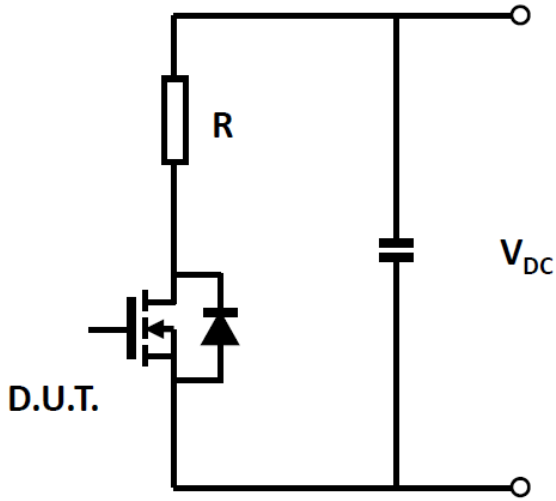


Figure 20. Schematic of Resistive Switching

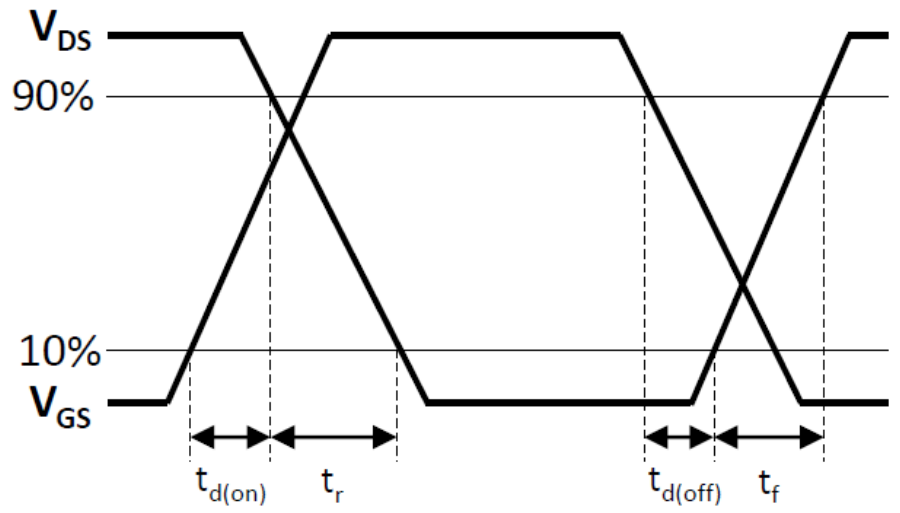
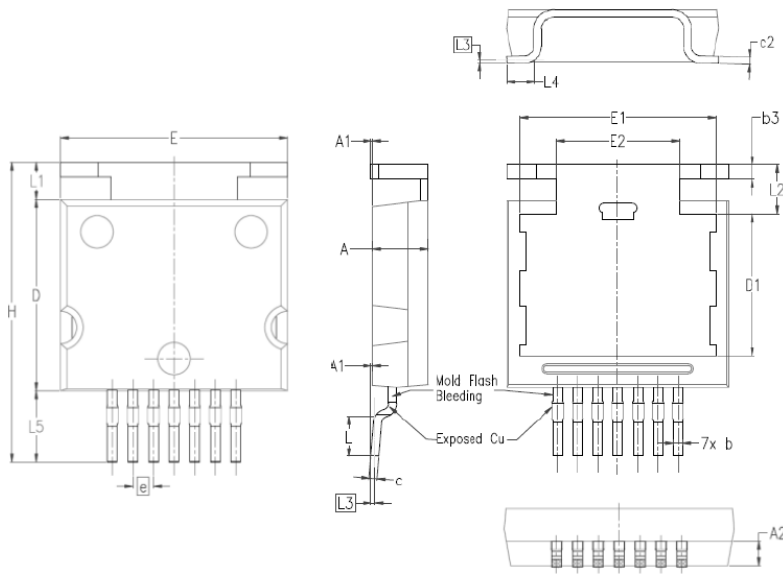


Figure 21. Switching Times Definition

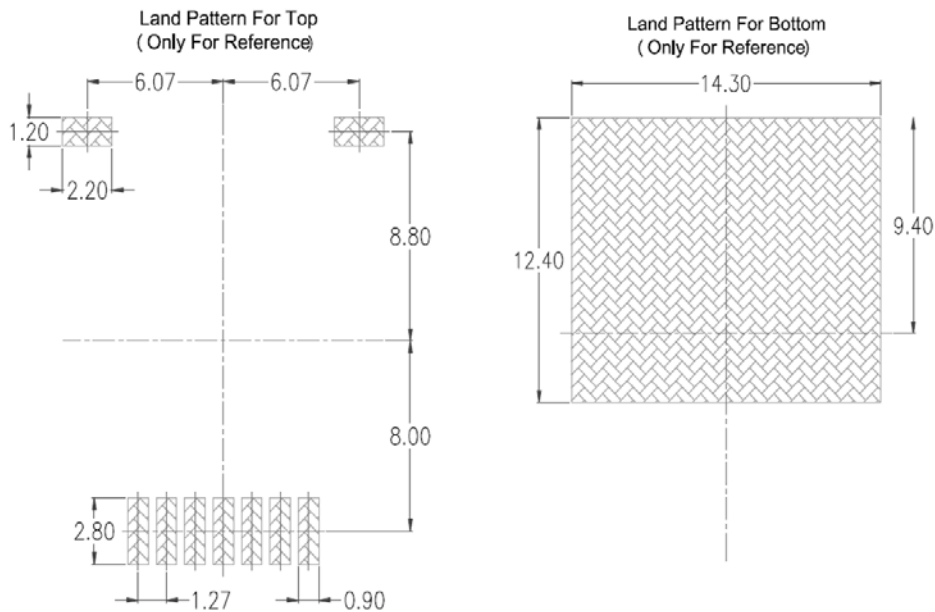


■ Outline Dimensions



Symbol	mm		
	Min.	Typ.	Max.
A	3.30	3.50	3.70
A1	---	0.10	0.25
A2	1.30	1.50	1.70
b	0.50	0.60	0.70
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.20
E	13.60	14.00	14.40
E1	12.00	12.40	12.80
E2	7.60	7.80	8.00
e	1.27 BSC		
H	17.70	18.50	19.30
L	1.90	2.50	3.10
L1	2.30 REF		
L2	2.85	3.10	3.35
L3	0.25 BSC		
L4	1.25	1.85	2.45
L5	4.40 REF		

■ Suggested Pad Layout







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